IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Jan Louis Josephina SERVAES, et al.	Attorney Docket Q65151
Appln. No. Not Assigned	Group Art Unit: Not Assigned
Confirmation No.:Not Assigned	Examiner: Not Assigned

Filed: July 23, 2001

For: LOW VOLTAGE BROADBAND LINE DRIVER

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicants hereby

notify the U.S. Patent and Trademark Office of the documents which are listed on the attached

Form PTO-1449 and/or listed herein and which the Examiner may deem material to patentability

of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith, along with a copy of the

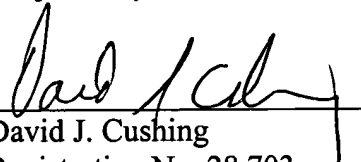corresponding Communication from a Foreign Patent Office.

The present Information Disclosure Statement is being filed: (1) No later than three

months from the application's filing date for an application other than a continued prosecution

application (CPA) under §1.53(d); (2) Before the mailing date of the first Office Action on the

merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a

INFORMATION DISCLOSURE STATEMENT
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request for continued examination (RCE) under §1.114, and therefore, no Statement under

37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

The submission of the listed documents is not intended as an admission that any such

document constitutes prior art against the claims of the present application. Applicants do not

waive any right to take any action that would be appropriate to antedate or otherwise remove any

listed document as a competent reference against the claims of the present application.

Respectfully submitted,

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Date: July 23, 2001

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date (if appropriate)

FOREIGN PATENT DOCUMENTS

		Document	Date	Country	Class	Sub-class	Translation Yes/No
		EP 0 901 221 A1	03/10/99	European			Yes
		WO 99/39433	08/05/99	World Office PCT			Yes

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		B. Schweber, "Analog Front Ends Bridge the XDSL-to-Real-World Chasm", EDN ELECTRICAL DESIGN NEWS, Massachusetts, US, Vol. 44, No. 7, 1 April 1999, pp. 48, 50, 52, 54, 56, 58, 60, 62, 64, XP000899059
		E. Nash, "Line-Driver Design for Broadband Communications Applications", ELECTRONIC DESIGN, us, Cleveland, Ohio, Vol., 45 , No. 27, 1 December 1997, pp. 81-82, 84, 86, 88 , XP000755760
		M. Steffes et al, "Optimizing Performance in an XDSL Line Driver', ELECTRONIC DESIGN, Cleveland, Ohio, US, Vol. 47, No. 8, 19 April 1999, pp. 44, 46, 48, 52, 54, 56, 58 XP000893613
		E. Nash, "Saving Dynamic Power in HDSL Line Driving Applications, ELECTRONIC DESIGN, US Cleveland Ohio, Vol., 45, No. 12, June 9, 1997, page 88, 90, 92, XP000731500

EXAMINER:	DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication.